

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

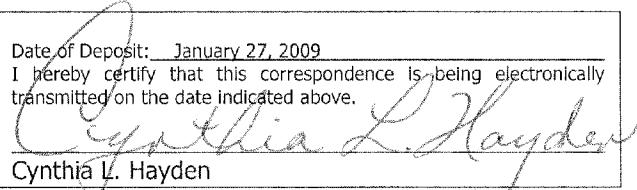
In re Applicant:	§	Art Unit:	2181
David K. Vavro et al.	§	Examiner:	Benjamin P. Geib
Serial No.:	§	Conf. No.:	9115
09/465,634	§	Atty Docket:	ITL.0286US
Filed:	§		P7814
December 17, 1999	§	Assignee:	Intel Corporation
For:	§		
Digital Signal Processor Having	§		
a Plurality of Independent	§		
Dedicated Processors	§		

Mail Stop Appeal Brief-Patents

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY BRIEF

The Examiner's Answer is based on a fundamental misapprehension of the applicable technology. The Examiner inexplicably insists that computers that do different operations have different instruction sets. This is simply untrue and the Examiner provides no support for this surprising conclusion. Computers that do different operations are simply programmed differently, even though they have the same instruction set. For example, one X86 processor may do one operation and another X86 processor with the same instruction set may do another operation. The reason for this is that they were programmed differently. That is why you have programmable devices. You can program them to do different operations. But the mere fact that they do different operations does not mean that they have different instruction sets.

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I hereby certify that this correspondence is being electronically
transmitted on the date indicated above.

Cynthia L. Hayden

Therefore, the rejection should be reversed.

Respectfully submitted,



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